

FAST: [Untitled1:1]

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☒ L1: (64281) MR or magnetoresist\$3  
☒ L2: (89) 1 and equivalent near3 circuit same induct\$4  
☒ L3: (55) 2 and high near3 frequency

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DBs

USPAT; US-PGPUB; EPD; JPO; DERVENT; BIL; TDB

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	64281	MR or magnetoresist\$3	USPAT; US-PGP UB; EP	2004/01/29 14:27		
2	BRS	L2	89	1 and equivalent near3 circuit same induct\$4	USPAT; US-PGP UB; EP	2004/01/29 14:29		
3	BRS	L3	55	2 and high near3 frequency	USPAT; US-PGP UB; EP	2004/01/29 14:29		

☒ HTML

☐ Details

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NUM



Find what: <input type="text" value="induct"/>		Find Next	
Area	Direction	Match word	Link in
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<input type="checkbox"/> Del/Cut	<input type="checkbox"/> Down	<input type="checkbox"/> Part	<input type="checkbox"/> Documents
			<input type="checkbox"/> Match case
		Cancel	

terminals (T3, T4) serve as output terminals. A current to change the magnetization direction of the magnetic tunnel resistance element (MR) is supplied through a current source (IP). With such a constitution, it is possible to provide an analog circuit, including a filter, a amplifier or the like, which can correct variations in value of elements caused by process variations in the manufacturing process.

#### Summary of Invention Paragraph - BSTX (3):

[0002] The present invention relates to a semiconductor memory device, and more particularly to a semiconductor device having a magnetic tunnel resistance element as a resistance element.

#### Summary of Invention Paragraph - BSTX (9):

[0008] In FIG. 40, the capacitor C is interposed between the terminals T1 and T3 and the resistor R is interposed between the wire connecting the terminals T2 and T4 and an electrode of the capacitor C on the side of the terminal T3. Further, the terminals T1 and T2 serve as input terminals and the terminals T3 and T4 serve as output terminals.

#### Summary of Invention Paragraph - BSTX (23):

[0021] In FIG. 44, the resistor R and an inductor L are interposed, being connected in series, between the terminals T1 and T3 and the capacitor C is interposed between the wire connecting the terminals T2 and T4 and an end portion of the inductor L on the side of the terminal T3.

#### Summary of Invention Paragraph - BSTX (30):

[0028] The LPF of FIG. 44 is represented by using the impedances Z1, Z2 and Z3 as shown in FIG. 46, and it is possible to form an LPF and an HPF by changing the combinations of passive elements (resistor, capacitor, inductor) which are assigned to these impedances.

FIG. 42  
< BACKGROUND ART >

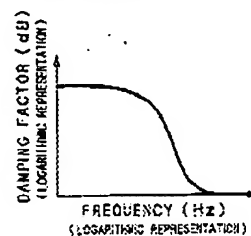


FIG. 43  
< BACKGROUND ART >

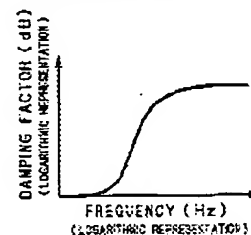
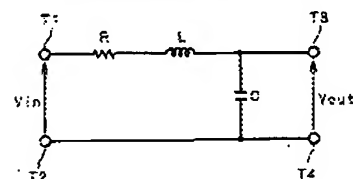


FIG. 44  
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GRAPHITIC NANOFIBERS IN  
ELECTROCHEMICAL CAPACITORS

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(52) U.S. Cl.

(57) **ABSTRACT**  
Graphitic nanofibers, which include tubular fullerenes called "buckytubes", nanotubes and are functionalized by chemical substitution, electrodes in electrochemical capacitors. The nanofiber based electrode increases the performance of electrochemical capacitors.

